



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/944,487	08/30/2001	Eugene H. Cloud	4296.1US (00-0054.1)	2698

24247 7590 05/20/2002

TRASK BRITT  
P.O. BOX 2550  
SALT LAKE CITY, UT 84110

EXAMINER

FARAHANI, DANA

ART UNIT

PAPER NUMBER

2814

DATE MAILED: 05/20/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application N .

09/944,487

Applicant(s)

CLOUD ET AL.

Examiner

Dana Farahani

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 28 March 2002.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-40 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-40 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 2, 4-10, 14-29, and 33-39 are rejected under 35 U.S.C. 102(b) as being anticipated by Buckley, III et al., hereinafter Buckley (U.S. 5,477,082).

Regarding claims 1, 2, and 4-8, Buckley discloses in figure 6 a method for interconnecting at least two semiconductor dice, 56 and 58A, comprising: providing a first semiconductor die 56, including a plurality of bond pads beneath the die, arranged in an array over an active surface thereof; providing at least one second semiconductor die 58A including a plurality of bond pads on an active surface thereof; orienting said first semiconductor die and said at least one second semiconductor die with said active surfaces thereof facing each other; electrically connecting at least some bond pads of said plurality of bond pads of said at least one second semiconductor die with corresponding bond pads of said first semiconductor die.

Regarding claims 9, 14-22, and 33-38, there is a carrier 60 with a plurality of contacts; wherein the active surface of the first semiconductor die faces the carrier, and the other bond pads of the first die is connected to contacts of the carrier.

Regarding claims 10 and 29, see column 6, lines 16-19.

Art Unit: 2814

Regarding claims 23-25 and 39, disclosed in figure 6 is a method for packaging a semiconductor device assembly, comprising: providing a first semiconductor die 56 including a plurality of bond pads arranged in an array over an active surface thereof; providing at least one second semiconductor die 58A including a plurality of bond pads arranged on an active surface thereof; orienting said at least one second semiconductor die over said first semiconductor die with said active surface facing said active surface of said first semiconductor die, said plurality of bond pads of said at least one second semiconductor die in alignment with corresponding bond pads of said first semiconductor die; providing a carrier 60 with a plurality of contacts; and orienting said first semiconductor die over said carrier with said active surface facing said carrier, bond pads of said first semiconductor die exposed beyond an outer periphery of said at least one second semiconductor die in alignment with corresponding contacts of said carrier.

Regarding claims 26-28, see column 3, lines 46-47.

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 3, 11-13, 30-32, and 40 are rejected under 35 U.S.C. 103(a) as being unpatentable over Buckley.

Regarding claim 3, it would have been within the level of ordinary skill in the art to use the second chip as a memory device.

Regarding claims 13 and 32, Buckley discloses the claimed invention except leads corresponding to the bond pads. It would have been obvious to one of ordinary skill in the art at the time the invention was made to include the leads in order to make external connections to the pads.

Regarding claim 40, Buckley discloses in figure 6 a first semiconductor die 56 and a second semiconductor die 90 where the bond pad of the second die is connected to the corresponding bond pad of the first die. Buckley does not disclose plurality of bond pads of the dice are connected to each other. It would have been obvious to one of ordinary skill in the art at the time the invention was made to include more connections to further connect the two chips.

5. Claims 11, 12, 30, and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Buckley as applied to claims 10 and 29 above, and further in view of Maurinus (U.S. 5,302,778).

Buckley discloses the claimed invention except the second chip being disposed in a recess in the chip carrier. Maurinus discloses in column 1, lines 15-25, connecting a substrate with a die via a recess forms a more compact structure. Therefore, It would have been obvious to one of ordinary skill in the art at the time the invention was made to use recess in the substrate carrier in order to form a more compact structure.


**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Farahani whose telephone number is (703)305-1914. The examiner can normally be reached on M-F 9:00AM - 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on (703)306-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9318 for regular communications and (703)872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

Dana Farahani  
May 13, 2002

  
OLIK CHAUDHURI  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800